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( Not for submission under 37 CFR 1.99)

Application Number	09878950
Filing Date	2001-06-11
First Named Inventor	Henry L. Sanders II
Art Unit	2194
Examiner Name	QING YUAN WU
Attorney Docket Number	MS1-0771US

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QW	1	5918226		1999-06-29	Tarumi et al.	

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QW	1	20020047899	A1	2002-04-25	Son et al.	
QW	2	20020062372	A1	2002-05-23	Hong et al.	
QW	3	20020069420	A1	2002-06-06	Russell et al.	
QW	4	20020078174	A1	2002-06-20	Sim et al.	
QW	5	20020087797	A1	2002-07-04	Adrangi	

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QW	6	20020120710	A1	2002-08-29	Chintalapati et al.	
QW	7	20020129123	A1	2002-09-12	Johnson et al.	
QW	8	20030061378	A1	2003-03-27	Mazzitelli	
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QW	2	6188698	B1	2001-02-13	Galand et al.	
QW	3	6542920	B1	2003-04-01	Belkin et al.	
QW	4	6594784	B1	2003-07-15	Harper et al.	
QW	5	6732138	B1	2004-05-04	Browning et al.	
QW	6	6810495	B2	2004-10-26	Castelli et al.	
QW	7	6820215	B2	2004-11-16	Harper et al.	
QW	8	6978398	B2	2005-12-20	Harper et al.	

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QW	2	MERCER et al., "Processor Capacity Reserves: An Abstraction for Managing Processor Usage", Workstation Operating Systems, 1993. Proceedings. Fourth Workshop on 14-15 Oct. 1993, Page(s): 129-134.	<input type="checkbox"/>
QW	3	ZHOU et al., "Processor Pool-Based Scheduling for Large-Scale NUMA Multiprocessors", Joint International Conference on Measurement and Modeling of Computer Systems, Pages: 133-142, Year of Publication: 1991	<input type="checkbox"/>

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